



INSTRUCTION MANUAL

SIL 3 Loop Powered 24 to 220 Vac/Vdc
Switch Repeater Transistor Out,
DIN-Rail and Termination Board,
Models D5093S, D5093D



General Description:

The single and dual channel 24 to 220 Vac/Vdc Loop powered Switch Repeater D5093S and D5093D modules are units suitable for applications requiring SIL 3 level (according to IEC 61508:2010 Ed.2) in safety related systems for high risk industries. Each channel is able to reflect the presence of a 24 to 220 Vac/Vdc input signal to the output by closing an optically coupled NO open-drain transistor (solid-state relay, MOSFET output). The presence of the 24 to 220 Vac/Vdc input signal is also indicated by a yellow LED on the front panel. The input switching voltage levels are selected, according to the applied input signal, by means of an internal dip-switch.

Mounting on standard DIN-Rail or on customized Termination Boards, in Safe Area / Non Hazardous Location or in Zone 2 / Class I, Division 2.

Functional Safety Management Certification:

G.M. International is certified by TÜV to conform to IEC61508:2010 part 1 clauses 5-6 for safety related systems up to and included SIL3.



Technical Data

Loop Input:

loop powered control signal.

Loop Supply: 24 to 220 Vac/Vdc nominal (18 to 250 Vac/Vdc), 50 mA fuse internally protected.

Current consumption: 3 mA/channel @ 250 Vac/Vdc nominal input.

Power consumption: 0.75 W per channel with 250 Vac/Vdc input signal.

Isolation (Test Voltage):

In/Out 1.5 kV; In/In 500 V; Out /Out 500 V.

Input switching voltage levels:

ON \geq 22 Vac/Vdc, OFF \leq 17 Vac/Vdc for 24 Vac/Vdc typical input

ON \geq 40 Vac/Vdc, OFF \leq 30 Vac/Vdc for 48 Vac/Vdc typical input

ON \geq 50 Vac/Vdc, OFF \leq 40 Vac/Vdc for 60 Vac/Vdc typical input

ON \geq 100 Vac/Vdc, OFF \leq 80 Vac/Vdc for 120 Vac/Vdc typical input

ON \geq 200 Vac/Vdc, OFF \leq 165 Vac/Vdc for 220 Vac/Vdc typical input

threshold level selection by means of internal dip-switch.

Output:

voltage free SPST optocoupled open-drain transistor (solid-state relay, MOSFET output).

Open-collector rating: 50 mA at 35 Vdc (\leq 1 Vdc voltage drop).

Leakage current: \leq 10 μ A at 35 Vdc.

Response time: \leq 75 ms.

Frequency response: 10 Hz maximum.

Compatibility:



CE mark compliant, conforms to Directive: 2014/34/EU ATEX, 2014/30/EU EMC, 2014/35/EU LVD, 2011/65/EU RoHS.

Environmental conditions:

Operating: temperature limits – 40 to + 70 °C, relative humidity 95 %, up to 55 °C.

Max altitude: 2000 m a.s.l.

Storage: temperature limits – 45 to + 80 °C.

Safety Description:



ATEX: II 3G Ex nA IIC T4 Gc

IECEx: Ex nA IIC T4 Gc

UKR TR n. 898: 2ExnAIICT4 X.

Approvals:

BVS 10 ATEX E 114 X conforms to EN60079-15, EN60079-0.

IECEx BVS 10.0072X conforms to IEC60079-15, IEC60079-0.

UL & C-UL E222308 conforms to UL 61010-1, UL 121201 for UL and CAN/CSA C22.2 No. 61010-1-12, CSA C22.2 No. 213-17 for C-UL.

CL 16.0036 X conforms to DCTY 7113, DCTY IEC 60079-15.

TÜV Certificate No. C-IS-224248-01 SIL 3 conforms to IEC61508:2010 Ed. 2.

TÜV Certificate No. C-IS-236198-09, SIL 3 Functional Safety Certificate conforms to IEC61508:2010 Ed.2, for Management of Functional Safety.

DNV No.A-13625 and KR No.MIL20769-EL002 for maritime applications.

Mounting:

T35 DIN-Rail according to EN50022, or on customized Termination Board.

Weight: about 135 g D5093D, 110 g D5093S.

Connection: by polarized plug-in disconnect screw terminal blocks to accommodate terminations up to 2.5 mm².

Location: installation in Safe Area/Non Hazardous Locations or Zone 2, Group IIC T4 or Class I, Division 2, Group A,B,C,D, T4.

Protection class: IP 20.

Dimensions: Width 12.5 mm, Depth 123 mm, Height 120 mm.

Ordering Information

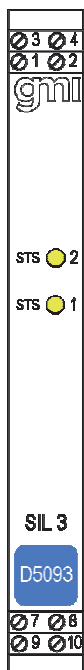
Model: D5093

DIN-Rail accessories: Cover and fix MCHP196

1 channel
2 channels

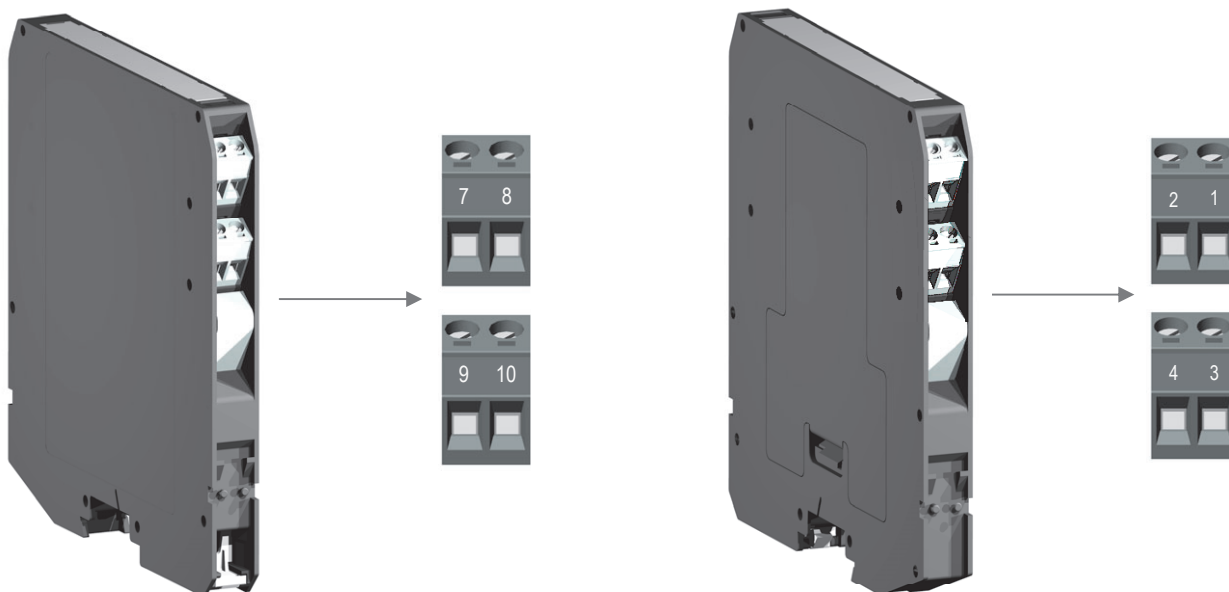
S
D

Front Panel and Features



- SIL 3 according to IEC 61508:2010 Ed.2 for Tproof = 6 / 20 years ($\leq 10\%$ / $> 10\%$ of total SIF)
- PFDavg (1 year) 1.54 E-05, SFF 96.53 %.
- Systematic capability SIL 3.
- 2 fully independent channels (only for D5093D).
- Installation in Zone 2 / Safe Area.
- Input/Output and Channel/Channel isolation.
- EMC Compatibility to EN61000-6-2, EN61000-6-4, EN61326-1, EN61326-3-1 for safety system.
- ATEX, IECEx, UL & C-UL, UKR TR n. 898, TÜV Certifications.
- TÜV Functional Safety Certification
- Type Approval Certificate DNV and KR for maritime applications.
- High Density, two channels per unit (D5093D).
- Simplified installation using standard DIN-Rail and plug-in terminal blocks, or on customized Termination Boards.

Terminal block connections



SAFE AREA

7

24 to 220 Vac/Vdc Input Ch1

8

9

24 to 220 Vac/Vdc Input Ch2 (only for D5093D)

10

1

+ Output Ch1

2

- Output Ch1

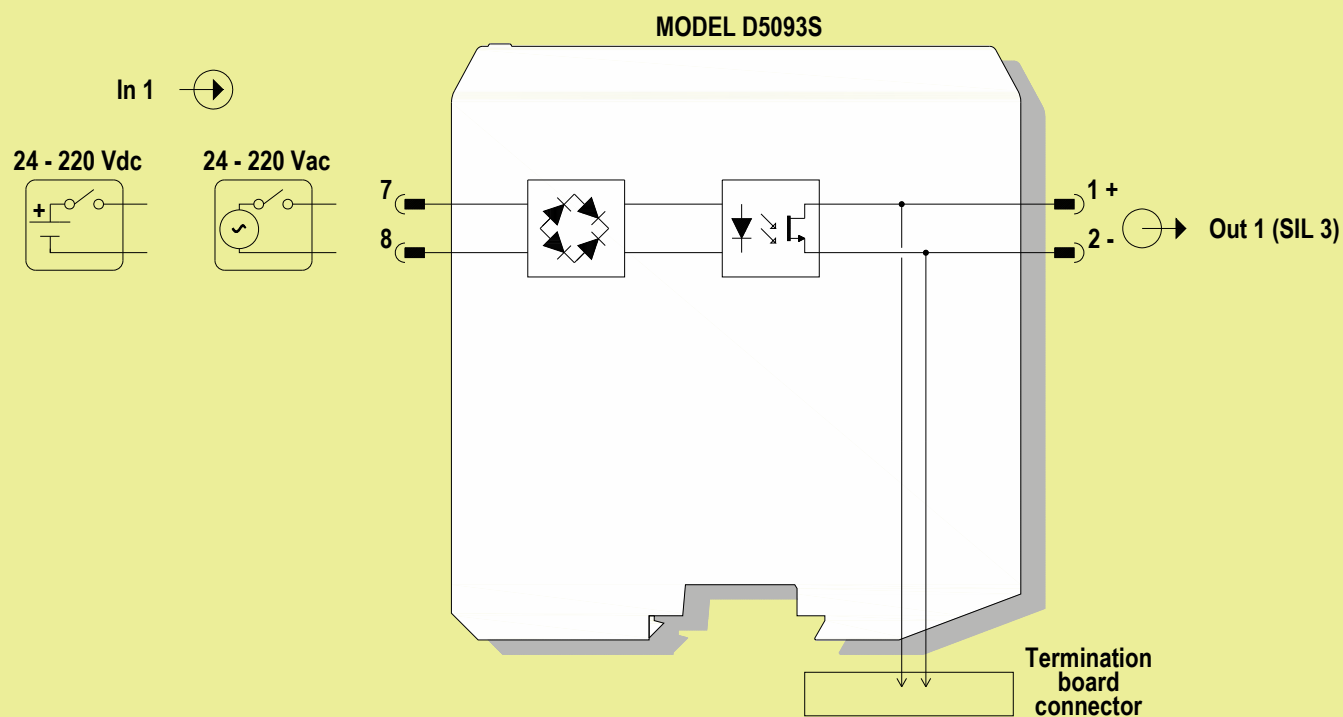
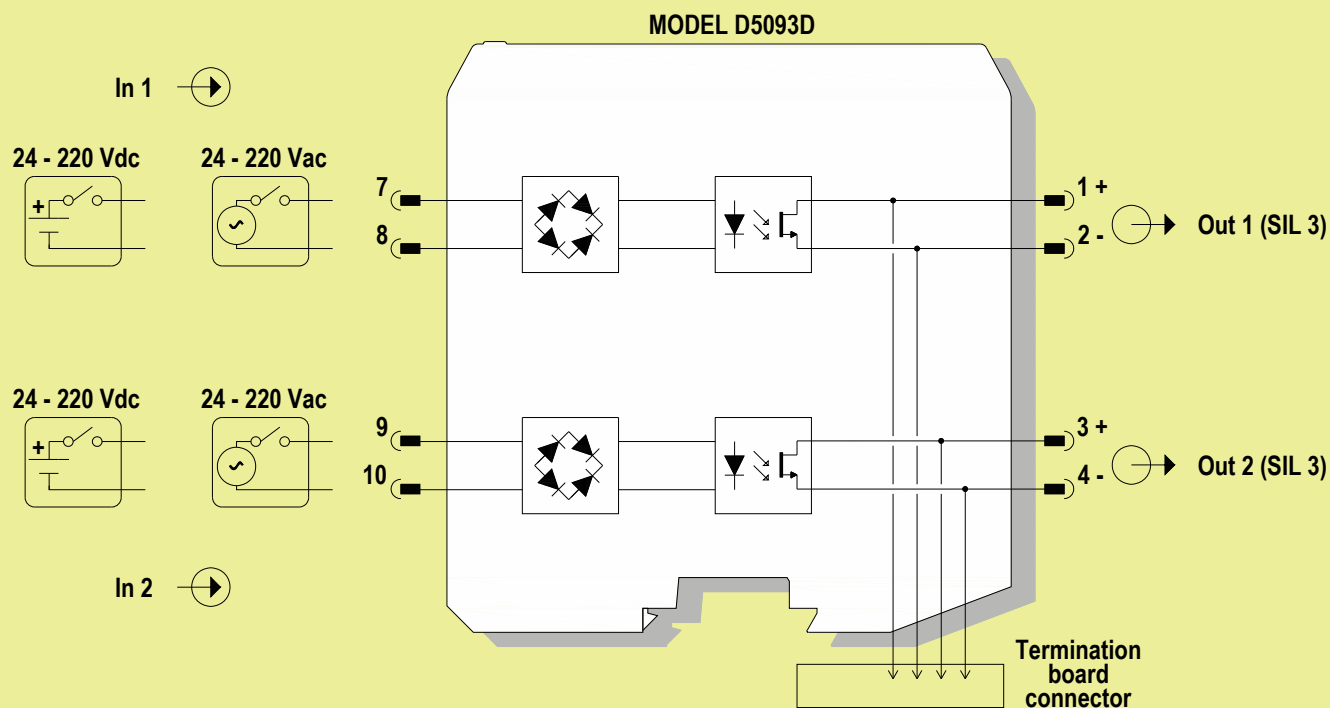
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+ Output Ch2 (only for D5093D)

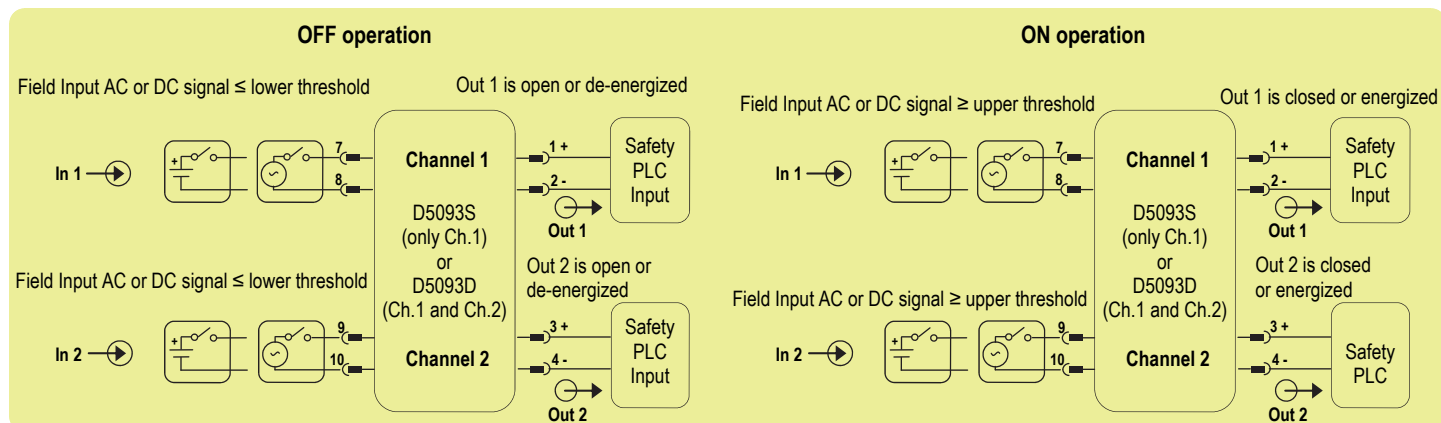
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- Output Ch2 (only for D5093D)

SAFE AREA, ZONE 2 GROUP IIC T4,
NON HAZARDOUS LOCATIONS, CLASS I, DIVISION 2,
GROUPS A, B, C, D T-Code T4



Application for D5093S or D5093D

**Description:**

Input AC or DC signals from field are applied to Pins 7-8 (In 1 - Ch.1) and Pins 9-10 (In 2 - Ch.2, only for D5093D). See table below for the lower and upper threshold levels. Open-drain transistor outputs Pins 1-2 (for Channel 1) and Pins 3-4 (for Channel 2, only for D5093D) are both normally open (or de-energized as safe state condition) for OFF operation, or they are both closed (or energized) for ON operation. See page 6 for DIP-switch configuration.

Typical input signal	Dip-switch position to be enabled (ON)	Lower threshold	Upper threshold
24 Vac / Vdc	1	17 Vac / Vdc	22 Vac / Vdc
48 Vac / Vdc	2	30 Vac / Vdc	40 Vac / Vdc
60 Vac / Vdc	3	40 Vac / Vdc	50 Vac / Vdc
110 Vac / Vdc	4	80 Vac / Vdc	100 Vac / Vdc
220 Vac / Vdc	5	165 Vac / Vdc	200 Vac / Vdc

The following table describes the state (open or closed) of each output when its input AC or DC signal is in the High (\geq upper threshold) or Low (\leq lower threshold) state.

Operation	Input Signal state Pins 7-8 (In 1 - Ch.1) or 9-10 (In 2 - Ch.2)	Output state Pins 1-2 (Out 1 - Ch.1) or 3-4 (Out 2 - Ch.2)
OFF	Low (\leq lower threshold)	Open (De-energized as safe state condition)
ON	High (\geq upper threshold)	Closed (Energized)

Safety Function and Failure behavior:

D5093 is considered to be operating in Low Demand mode, as a Type A module, having Hardware Fault Tolerance (HFT) = 0.

The failure behaviour is described from the following definitions :

- fail-Safe State: it is defined as the transistor output being de-energized or open;
- fail Safe: failure mode that causes the module to go to the defined fail-safe state without a demand from the process;
- fail Dangerous: failure mode that does not respond to a demand from the process (i.e. being unable to go to the defined fail-safe state), so that the transistor output remains energized or closed;
- fail "No Effect": failure mode of a component that plays a part in implementing the safety function but that is neither a safe failure nor a dangerous failure. When calculating the SFF this failure mode is not taken into account;
- fail "Not part": failure mode of a component which is not part of the safety function but part of the circuit diagram and is listed for completeness. When calculating the SFF this failure mode is not taken into account.

The 2 channels of the D5093D module could be used to increase the hardware fault tolerance, needed for a higher SIL of a certain Safety Function, as they are completely independent from each other, not containing common components. In fact, the analysis results got for D5093S (single channel) are also valid for each channel of D5093D (double ch.).

Failure rate table:

Failure category	Failure rates (FIT)
λ_{dd} = Total Dangerous Detected failures	0.00
λ_{du} = Total Dangerous Undetected failures	3.52
λ_{sd} = Total Safe Detected failures	0.00
λ_{su} = Total Safe Undetected failures	98.05
$\lambda_{tot\ safe}$ = Total Failure Rate (Safety Function) = $\lambda_{dd} + \lambda_{du} + \lambda_{sd} + \lambda_{su}$	101.57
MTBF (safety function, single channel) = $(1 / \lambda_{tot\ safe}) + MTTR$ (8 hours)	1123 years
$\lambda_{no\ effect}$ = "No Effect" failures	98.33
$\lambda_{not\ part}$ = "Not Part" failures	2.90
$\lambda_{tot\ device}$ = Total Failure Rate (Device) = $\lambda_{tot\ safe} + \lambda_{no\ effect} + \lambda_{not\ part}$	202.80
MTBF (device, single channel) = $(1 / \lambda_{tot\ device}) + MTTR$ (8 hours)	562 years

NOTE: These values are always valid for each of 5 possible thresholds.

Failure rates table according to IEC 61508:2010 Ed.2 :

λ_{sd}	λ_{su}	λ_{dd}	λ_{du}	SFF
0.00 FIT	98.05 FIT	0.00 FIT	3.52 FIT	96.53%

PFDavg vs T[Proof] table (assuming Proof Test coverage of 95%), with determination of SIL supposing module contributes $\leq 10\%$ of total SIF dangerous failures:

T[Proof] = 1 year	T[Proof] = 6 years
PFDavg = 1.54 E-05 - Valid for SIL 3	PFDavg = 9.27 E-05 - Valid for SIL 3

PFDavg vs T[Proof] table (assuming Proof Test coverage of 95%), with determination of SIL supposing module contributes $> 10\%$ of total SIF dangerous failures:

T[Proof] = 20 years
PFDavg = 3.09 E-04 - Valid for SIL 3

Systematic capability SIL 3.

Testing procedure at T-proof

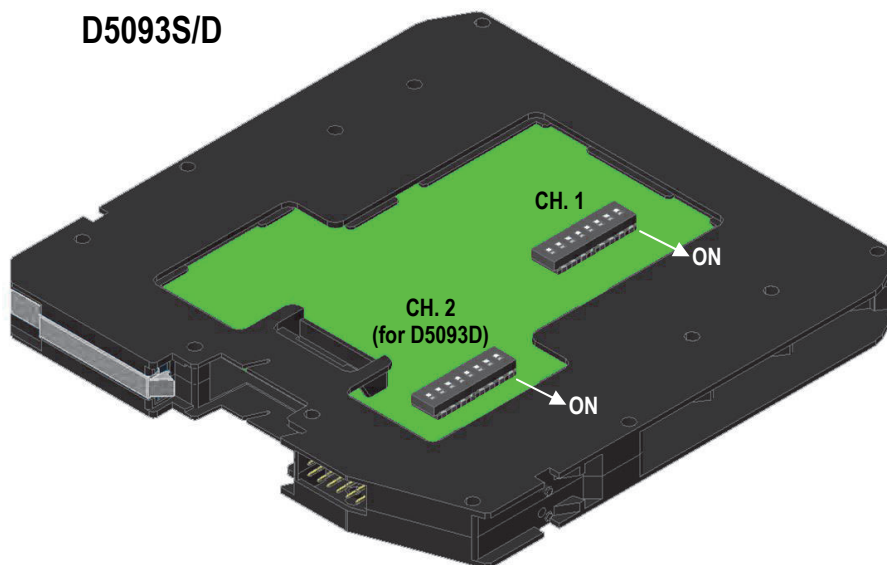
The proof test shall be performed to reveal dangerous faults which are undetected by diagnostic. This means that it is necessary to specify how dangerous undetected faults, which have been noted during the FMEDA, can be revealed during proof test. The Proof test consists of the following steps:

Steps	Action
1	Bypass the safety-related PLC or take any other appropriate action in order to avoid a false trip.
2	<p>Connect a variable DC / AC source to any input terminals couple (7-8 for D5093S and 7-8, 9-10 for D5093D). Connect the positive output terminals (1 for D5093S and 1, 3 for D5093D) to the series of a current limiting resistor (i.e. 1 kΩ) and the positive pole of a DC voltage generator (i.e. 24 Vdc); then connect the output negative terminals (2 for D5093S and 2, 4 for D5093D) to the negative pole of the DC voltage generator. Measure, by means of a DC voltmeter, the voltage difference between any output terminals couple (1-2 for D5093S and 1-2, 3-4 for D5093D).</p> <p>During normal operation, when the input DC / AC voltage amplitude is comprised between 18 Vdc / Vrms and 250 Vdc / Vrms (maximum allowed value for the module safe operation) and the appropriate threshold is selected, by means of the internal dip-switch, in accordance with the applied input signal, the output open-drain transistor is in the closed state. In this condition, a voltage drop lower than 1.0 Vdc should be measured between the output terminals and the yellow status LED is lit.</p> <p>Then, when the input DC / AC voltage amplitude falls below the related lower threshold value, the output transistor turns to the open state. In this condition, a 24 Vdc voltage drop should be measured between the output terminals and the yellow status LED is turned off. Once the lower threshold voltage is reached, the following two possible fault situations can happen:</p> <ol style="list-style-type: none"> 1) if the status LED has been turned off, but a low value voltage is still measured between the output terminals, then a dangerous failure which has shorted the output terminals is detected; 2) if the status LED is lit again and a low value voltage is still measured between the output terminals, then a dangerous failure which has excessively lowered the comparator stage threshold is detected. <p>Repeat this test for all of the five possible threshold voltage levels (each one of them corresponds to a different position of the dip-switch selector).</p>
3	Use the same setup described in the previous step and measure, by means of an AC voltmeter, the rms value of the output voltage. In normal operation, both when the output open-drain transistor is in the open or closed state, the output voltage measured at terminals 1-2 of D5093S or 1-2, 3-4 of D5093D should have no AC components, that is its rms value should be ideally null. If an rms value well above 0 Vrms is measured (a reasonable value could be 50% of the voltage which supplies the output circuit, i.e. 12 Vrms compared to 24 Vdc), a dangerous failure which has produced an oscillation of the internal comparator stage output is detected.
4	Restore the loop to full operation.
5	Remove the bypass from the safety-related PLC or restore normal operation.

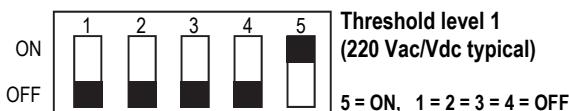
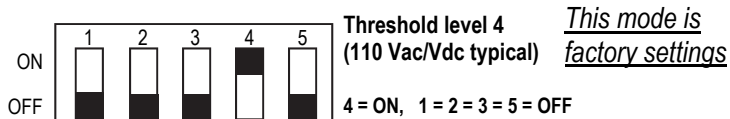
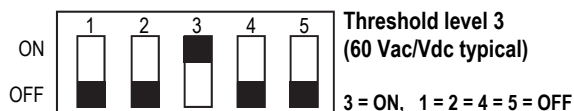
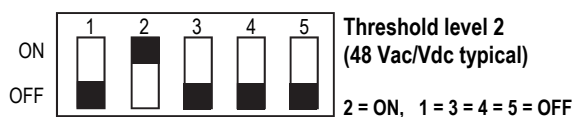
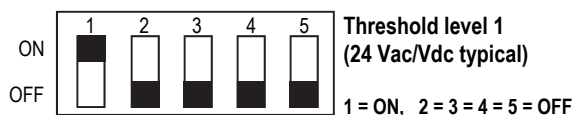
This test reveals almost 95 % of all possible Dangerous Undetected failures in the module.

Configuration

D5093S/D



DIP switch configurations (all valid for SIL applications):



DIP switch positions 6 to 8 are not used.

Warning

D5093 series is electrical apparatus installed into standard EN/IEC60715 TH 35 DIN-Rail located in Safe Area or Zone 2, Group IIC, Temperature Classification T4 or Class I, Division 2, Group A, B, C, D, T4 Hazardous Area within the specified operating temperature limits Tamb - 40 to +70 °C.

D5093 series must be installed, operated and maintained only by qualified personnel, in accordance to the relevant national/international installation standards (e.g. IEC/EN60079-14 Electrical apparatus for explosive gas atmospheres - Part 14: Electrical installations in hazardous areas (other than mines)), following the established installation rules.

De-energize power source (turn off power supply voltage) before plug or unplug the terminal blocks when installed in Hazardous Area or unless area is known to be nonhazardous.

Warning: substitution of components may impair suitability for Zone 2/Division 2. Avertissement: la substitution des composants peut nuire à l'aptitude à la Zone 2/Div. 2.

Explosion Hazard: to prevent ignition of flammable atmospheres, disconnect power before servicing or unless area is known to be nonhazardous. Danger d'Explosion: pour éviter l'inflammation d'atmosphères inflammables, débrancher l'alimentation avant l'entretien ou à moins que région est connue pour être non dangereuse.

Warning: de-energize main power source (turn off power supply voltage) and disconnect plug-in terminal blocks before opening the enclosure to avoid electrical shock when connected to live hazardous potential. Avertissement: débrancher l'alimentation (couper la tension d'alimentation) et les blocs de jonction enfichables avant d'ouvrir le boîtier pour éviter les chocs électriques lorsqu'ils sont connectés à un potentiel dangereux.

Failure to properly installation or use of the equipment may risk to damage the unit or severe personal injury.

The unit cannot be repaired by the end user and must be returned to the manufacturer or his authorized representative. Any unauthorized modification must be avoided.

Operation

The single and dual channel 24 to 220 Vac / Vdc Loop powered Switch Repeater, D5093S and D5093D module is a unit suitable for applications requiring SIL 3 level (according to IEC 61508:2010 Ed. 2) in safety related systems for high risk industries.

Each channel is able to reflect the presence of a 24 to 220 Vac / Vdc input signal to the output by closing an optocoupled NO open-drain transistor.

The presence of the input signal is also indicated by a "STATUS CHANNEL" Yellow LED on the front panel.

Installation

D5093 series are 24 to 220 Vac / Vdc Loop powered Switch Repeater modules housed in a plastic enclosure suitable for installation on EN/IEC60715 TH 35 DIN-Rail or on customized Termination Board.

D5093 series can be mounted with any orientation over the entire ambient temperature range.

Electrical connection are accommodated by polarized plug-in removable screw terminal blocks which can be plugged in/out into a powered unit without suffering or causing any damage **(for Zone 2 installations check the area to be nonhazardous before servicing)**. Connect only one individual conductor per each clamping point, use conductors up to 2.5 mm² (13 AWG) and a torque value of 0.5-0.6 Nm. Use only cables that are suitable for a temperature of at least 85°C. The wiring cables have to be proportionate in base to the current and the length of the cable.

On the section "Function Diagram" and enclosure side a block diagram identifies all connections.

Identify the number of channels of the specific card (e.g. D5093S is a single channel model and D5093D is a dual channel model), the function and location of each connection terminal using the wiring diagram on the corresponding section, as an example:

For D5093S model connect 24 to 220 Vac / Vdc loop powered lines of channel 1 to the terminals "7" and "8".

For D5093D model, in addition to channel 1 connections above, connect 24 to 220 Vac / Vdc loop powered lines of channel 2 to the terminals "9" and "10".

For D5093S model connect positive output of channel 1 at terminal "1" and negative output at "2".

For D5093D model, in addition to channel 1 connection above, connect terminal "3" for positive and "4" for negative on channel 2.

Installation and wiring must be in accordance to the relevant national or international installation standards (e.g. IEC/EN60079-14 Electrical apparatus for explosive gas atmospheres Part 14: Electrical installations in hazardous areas (other than mines)), make sure that conductors are well isolated from each other and do not produce any unintentional connection.

Connect output transistors checking the load rating to be within the maximum rating (50 mA at 35 Vdc (≤ 1.0 Vdc voltage drop)).

The enclosure provides, according to EN60529, an IP20 minimum degree of protection (or similar to NEMA Standard 250 type 1). The unit shall be installed in an area of no more than pollution degree 2 according to EN/IEC60664-1. When installed in EU Zone 2, the unit shall be mounted in a certified Ex enclosure that provides a degree of protection not less than IP54 in accordance with EN/IEC60079-15. When installed in a Class I, Division 2 Hazardous Location, the unit shall be mounted in a supplemental enclosure that provides a degree of protection not less than IP54. The enclosure must have a door or cover accessible only by the use of a tool. The end user is responsible to ensure that the operating temperature of the module is not exceeded in the end use application.

Units must be protected against dirt, dust, extreme mechanical (e.g. vibration, impact and shock) and thermal stress, and casual contacts. If enclosure needs to be cleaned use only a cloth lightly moistened by a mixture of detergent in water.

Electrostatic Hazard: to avoid electrostatic hazard, the enclosure of D5093 must be cleaned only with a damp or antistatic cloth.

Any penetration of cleaning liquid must be avoided to prevent damage to the unit. Any unauthorized card modification must be avoided.

All circuits connected to D5254 series must comply with the overvoltage category II (or better) according to EN/IEC60664-1.

Warning: de-energize main power source (turn off power supply voltage) and disconnect plug-in terminal blocks before opening the enclosure to avoid electrical shock when connected to live hazardous potential.

Start-up

Before powering the inputs of unit check that all wires are properly connected, also verifying their polarity. Check conductors for exposed wires that could touch each other causing dangerous unwanted shorts. When loop powered signal is applied to input channel with a value more than 22 Vac / Vdc for threshold 1, 40 Vac / Vdc for threshold 2, 50 Vac / Vdc for threshold 3, 100 Vac / Vdc for threshold 4, 200 Vac / Vdc for threshold 5, the corresponding transistor output must be closed and the "STATUS CHANNEL" yellow LED must be lit. Instead, when loop powered signal is less than 17 Vac / Vdc for threshold 1, 30 Vac / Vdc for threshold 2, 40 Vac / Vdc for threshold 3, 80 Vac / Vdc for threshold 4, 165 Vac / Vdc for threshold 5, the corresponding transistor output must be open and the "STATUS CHANNEL" yellow LED must be turned off.